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U.S. Patent Application No.: 10/716,595

Attorney Docket No.: 57941.000062

Client Reference No.: RA001.2003.1.C.US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: :
: Group Art Unit: 2827
Michael Farmwald et al. :
: Examiner: Tan Nguyen
Appln. No.: 10/716,595 :
: Confirmation No.: 7213
Filed: November 20, 2003 :
: Customer No.: 38013
For: MEMORY DEVICE AND METHOD :
FOR OPERATING SAME :

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicants hereby submits attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

Per the voice communication between the Examiner and the undersigned on September 29, 2006, the filing of this supplemental information disclosure statement was necessitated by the filing of Requests for Inter Partes Reexamination for

related U.S. Patent Nos. 6,426,916 and 6,324,120. The Examiner may note that many of the listed references are dated after the effective filing date of the above-identified patent application. The Requests for Inter Partes Reexamination allege that such references are relevant because the claims presented in the respective patents are not entitled to their effective filing dates. Many of the other references and arguments presented in the Requests for Inter Partes Reexamination were considered in U.S. District Court and U.S. Appellate Court patent litigation proceedings involving related patents. Such proceedings, which included claim construction proceedings for the related patents, include *Rambus Inc. v. Infineon Technologies AG et al.*, No. 3:00cv524 (E.D. Vir. 2001), *Rambus Inc. v. Infineon Technologies AG et al.*, 318 F.3d 1081 (Fed. Cir. 2003), and *Hynix Semiconductor, Inc. et al. v. Rambus Inc.*, No. CV 00-20905 RMW (N.D. Cal. 2005). The Hynix case specifically included the patents for which reexamination has been requested, and many of the arguments and references relied upon in the Requests for Inter Partes Reexamination were presented in those proceedings. (See e.g., reference citation nos. 17 and 31-33). If the Examiner believes that additional information relating to such proceedings would be helpful in determining patentability of the above-identified patent

application, Applicants would gladly provide same.

Applicants hereby expressly reserve the right to swear behind the effective dates of any of the cited references. Applicants also reserve the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this supplemental information disclosure statement. Further, the filing of this supplemental information disclosure statement is not to be construed as a representation that a search has, or has not, been conducted or that no better art exists.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicants.

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may

be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. Any such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 C.F.R. § 1.491; (iii) before the mailing date of a first Office Action on the merits for the above-identified patent application; or (iv) before the mailing date of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

U.S. Patent Application No.: 10/716,595
Attorney Docket No.: 57941.000062
Client Reference No.: RA001.2003.1.C.US

Respectfully Submitted,

Hunton & Williams LLP

By: 

Thomas E. Anderson

Registration No. 37,063

TEA/asc

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Date: October 2, 2006

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number	10/716,595
Filing Date	November 20, 2003
First Named Inventor	Michael Farmwald
Art Unit	2827
Examiner Name	Tan Nguyen
Attorney Docket Number	57941.000062

Sheet	1 of 5
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[illegible]

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[illegible]

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
		First Named Inventor	Michael Farmwald
		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	3 of 5	Attorney Docket Number	57941.000062

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	4.	Hyundai, HY5DV651622, Rev. 0.9, published January 2000 ("Hynix-1")	<input type="checkbox"/>	<input type="checkbox"/>
	5.	Hyundai, SDRAM Timing Diagram, Rev.1.2, published December 1999 ("Hynix-2")	<input type="checkbox"/>	<input type="checkbox"/>
	6.	Hyundai, DDR SDRAM Device Operation, Rev. 0.2, published December 1998 ("Hynix-3")	<input type="checkbox"/>	<input type="checkbox"/>
	7.	Claim Chart for Claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 40-41 based on Park, with additional references to the JEDEC Standard	<input type="checkbox"/>	<input type="checkbox"/>
	8.	Claim Chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard	<input type="checkbox"/>	<input type="checkbox"/>
	9.	Claim Chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover	<input type="checkbox"/>	<input type="checkbox"/>
	10.	Claim Chart for claims 1-41 of the '916 patent based on the Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover	<input type="checkbox"/>	<input type="checkbox"/>
	11.	Patent Family Tree Chart	<input type="checkbox"/>	<input type="checkbox"/>
	12.	File History of the '916 patent	<input type="checkbox"/>	<input type="checkbox"/>
	13.	File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history)	<input type="checkbox"/>	<input type="checkbox"/>
	14.	Rambus Inc. v. Samsung Electronics Co., Ltd., et al., No. C05 02298 RMW (N. D. Cal. 2006), Preliminary Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE	DATE CONSIDERED
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
		First Named Inventor	Michael Farmwald
		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	4 of 5	Attorney Docket Number	57941.000062

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	15.	Samsung Electronics Co., Ltd., v. Rambus Inc., No. 3:05cv406 (E. D. Va. 2006), Memorandum Opinion filed July 18, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	16.	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005), Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3	<input type="checkbox"/>	<input type="checkbox"/>
	17.	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005), Claim Construction Order filed Nov. 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	18.	Selected Excerpts from the Trial Transcript from Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005)	<input type="checkbox"/>	<input type="checkbox"/>
	19.	Rambus Inc. v. Infineon Technologies AG et al., No. 3:00cv524 (E. D. Vir. 2001), Memorandum Opinion	<input type="checkbox"/>	<input type="checkbox"/>
	20.	Rambus Inc. v. Infineon Technologies AG et al., 318 F.3d 1081 (Fed. Cir. 2003)	<input type="checkbox"/>	<input type="checkbox"/>
	21.	Waters, "Hynix Told to Pay Damages to Rambus", Financial Times, April 24, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	22.	Poletti, "Rambus wins victory from Supreme Court - Win could Lead to Success for Rambus' Other Lawsuits seeking Royalties", San Jose Mercury News, October 7, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	23.	Kanellos, "Future of memory market hangs on Rambus trials - It's the trial of the century, at least as far as the memory industry is concerned, CNET News.com, February 12, 2001, http://news.com.com/Future+of+memory+market+hangs+on+Rambus+trials/2100-1001_3-252404.html	<input type="checkbox"/>	<input type="checkbox"/>
	24.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Opinion of the Commission	<input type="checkbox"/>	<input type="checkbox"/>
	25.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Concurring Opinion of Commission Jon Leibowitz	<input type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE

DATE CONSIDERED

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
		First Named Inventor	Michael Farmwald
		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	5 of 5	Attorney Docket Number	57941.000062

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	26.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Order Reversing and Vacating Initial Decision...	<input type="checkbox"/>	<input type="checkbox"/>
	27.	Rambus' Final Infringement Contentions in the Hynix Litigation	<input type="checkbox"/>	<input type="checkbox"/>
	28.	Exhibit A (accused products) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	29.	Exhibit P ('916 infringement chart) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	30.	Exhibit R (Exemplary data sheet) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	31.	Request for <i>Inter Partes</i> Reexamination under 35 U.S.C. §§ 311-318 for U.S. Patent No. 6,324,120.	<input type="checkbox"/>	<input type="checkbox"/>
	32.	Request for <i>Inter Partes</i> Reexamination under 35 U.S.C. §§ 311-318 for U.S. Patent No. 6,426,916.	<input type="checkbox"/>	<input type="checkbox"/>
	33.	Special Verdict Form filed April 24, 2006, in Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005)	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>

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In place of PTO-1449 Form		U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Ex Parte Reexamination of 6,426,916
				Filing Date	Herewith
				Applicant(s)	...
				Art Unit	To be Determined
				Examiner Name	To be Determined
SHEET	1 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	38512.3

U. S. PATENT DOCUMENTS				
Examiner's Initials	Exhibit	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
	A	6,426,916	07-30-2002	Farmwald, et al.
	B	5,590,086	12-31-1996	Park, et al.
	F	4,480,307	10-30-1984	Budde, et al.
	G	4,315,308	02-09-1982	Jackson
	J	4,933,910	06-12-1990	Olson, et al.
	K	5,361,277	03-30-1989	11-01-1994

FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Exhibit	Foreign Patent Document	Publication Date MM-DD-YYYY	Patentee or Applicant of Cited Document	Translation Y/N
	I	JP S56-047996	04-30-1981	Yoshida	Yes
	L	GB 2197553	05-18-1988	Lofgren, et al.	

NON-PATENT LITERATURE DOCUMENTS		
Examiner's Initials	Exhibit	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	C	Joint Electron Device Engineering Council (JEDEC) Standard No. 21-C, Revision 9 published in 1999 ("JEDEC Standard")
	D, Tab 1	Hyundai, <u>HY5DV651622</u> , Rev. 0.9, published January 2000 ("Hynix -1")
	D, Tab 2	Hyundai, <u>SDRAM Timing Diagram</u> , Rev. 1.2, published December 1999 ("Hynix -2")
	D, Tab 3	Hyundai, <u>DDR SDRAM DEVICE OPERATION</u> , Rev. 0.2, published December 1998 ("Hynix -3")
	E, Tab 1	Intel Corporation, <u>iAPX 432 Interconnect Architecture Reference Manual</u> , published in 1982 ("the iAPX Manual")
	E, Tab 2	Intel Corporation, <u>Electrical Specifications for iAPX 43204 Bus-Interface Unit (BIU) and iAPX 43205 memory control unit (MCU)</u> , published March 1983 ("the iAPX Specification")
	H	Rau et al., <u>The Cydra 5 Departmental Supercomputer Design Philosophies, Decisions, and Tradeoffs</u> , published in January 1989 ("Rau")

Examiner Signature		Date Considered	
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Customer No. 000027683

In place of PTO-1449 Form		U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	Ex Parte Reexamination of 5,794,060
				Filing Date	Herewith
				Applicant(s)	...
				Art Unit	To be Determined
				Examiner Name	To be Determined
SHEET	2 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	33142.9

NON-PATENT LITERATURE DOCUMENTS		
Examiner's Initials	Exhibit	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	M	Johnson et al., <u>A Variable Delay Line PLL for CPU - Coprocessor Synchronization</u> , IEEE Journal of Solid-State Circuits, vol. 23, no. 5, published October 1988 ("Johnson")
	N	Intel Corporation, <u>Memory Components Handbook</u> , Chapter 1 and Chapter 3, Application Note AP-132, published in 1982 and 1985 ("the iRAM reference")
	O	Claim chart for claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 39-41 based on Park, with additional reference to the JEDEC Standard
	P	Claim chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard
	Q	Claim chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover
	R	Claim chart for claims 1-41 of the '916 patent based on Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover
	S	Patent Family Tree Chart
	T	File History of the '916 patent
	U	File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history)
	V	<i>Rambus Inc. v. Samsung Electronics Ltd., et al.</i> No. C 05 02298 RMW (N.D. Cal. 2006), Preliminary Infringement Contentions
	W	<i>Samsung Electronics Co., Ltd., v. Rambus Inc.</i> No. 3:05cv406 (E.D. Va. 2006), Memorandum Opinion filed July 18, 2006
	X	<i>Hynix Semiconductor, Inc. et al. v. Rambus Inc.</i> , No. CV 00-20905 RMW (N.D. Cal. 2005), Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3
	Y	<i>Hynix Semiconductor, Inc. et al. v. Rambus Inc.</i> , No. CV 00-20905 RMW (N.D. Cal. 2005), Claim Construction Order filed Nov. 15, 2004
	Z	Selected Excerpts from the Trial Transcript from <i>Hynix Semiconductor, Inc. et al. v. Rambus Inc.</i> , No. CV 00-20905 RMW (N.D. Cal. 2005).
	AA	<i>Rambus Inc. v. Infineon Technologies et al.</i> , No. 3:00cv524 (E.D. Vir. 2001), Memorandum Opinion
	AB	<i>Rambus Inc. v. Infineon Technologies AG</i> , 318 F.3d 1081 (Fed. Cir. 2003)

Examiner Signature		Date Considered	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Ex Parte Reexamination of 5,794,060
				Filing Date	Herewith
				Applicant(s)	...
				Art Unit	To be Determined
				Examiner Name	To be Determined
SHEET	3 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	33142.9

NON-PATENT LITERATURE DOCUMENTS		
Examiner's Initials	Exhibit	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	AC	Waters, <u>Hynix Told To Pay Damages To Rambus</u> , Financial Times, April 24 2006
	AD	Poletti, <u>Rambus wins victory from Supreme Court - Win Could Lead To Success For Rambus' Other Lawsuits Seeking Royalties</u> , San Jose Mercury News, Oct. 07, 2003
	AE	Kanellos, <u>Future of memory market hangs on Rambus trials - It's the trial of the century, at least as far as the memory industry is concerned</u> , CNET News.com, Feb. 12, 2001, http://news.com.com/Future+of+memory+market+hangs+on+Rambus+trials/2100-1001_3-252404.html
	AF	Appeal Board Decision dated Feb. 12, 2004, Summary of Facts and Submissions for the oppositions filed against European Patent No. 0 525 068
	AG	Summary of Facts and Submissions for the oppositions filed against European Patent No. 1 004 956
	AH, Tab 1	In the Matter of Rambus, Inc., FTC Docket No. 9302, Opinion of the Commission
	AH, Tab 2	In the Matter of Rambus, Inc., FTC Docket No. 9302, Concurring Opinion of Commissioner Jon Leibowitz
	AH, Tab 3	In the Matter of Rambus, Inc., FTC Opinion Docket No. 9302, Order Reversing and Vacating Initial Decision ...
	AI, Tab 1	Rambus' Final Infringement Contentions in the Hynix Litigation
	AI, Tab 2	Exhibit A (accused products) to Rambus' Final Infringement Contentions
	AI, Tab 3	Exhibit P ('916 infringement chart) to Rambus' Final Infringement Contentions
	AI, Tab 4	Exhibit R (Exemplary data sheet) to Rambus' Final Infringement Contentions
	AJ	CD-ROM including present Request for <i>Inter Partes</i> Reexamination and Exhibits

Examiner Signature		Date Considered	
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